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Response Under 37 C.F.R. §1.116
Expedited Procedure Examining
Examining Group 2123

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Daniel R. Knebel, et al.

Examiner: Fred O. Ferris, III

Serial No.: 09/406,663

Art Unit: 2123

Filed: September 27, 1999

Docket: YOR919990438US1 (13031)

For: SYSTEM AND METHOD FOR
VLSI VISUALIZATION

Dated: November 26, 2003

Commissioner for Patents
P.O. Box 1450
Arlington, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. §1.116

Sir:

In response to the Office Action dated August 28, 2003, please amend the subject patent application as follows.

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450 on November 26, 2003:

Dated: November 26, 2003

John S. Sensny
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